

FIG. 1

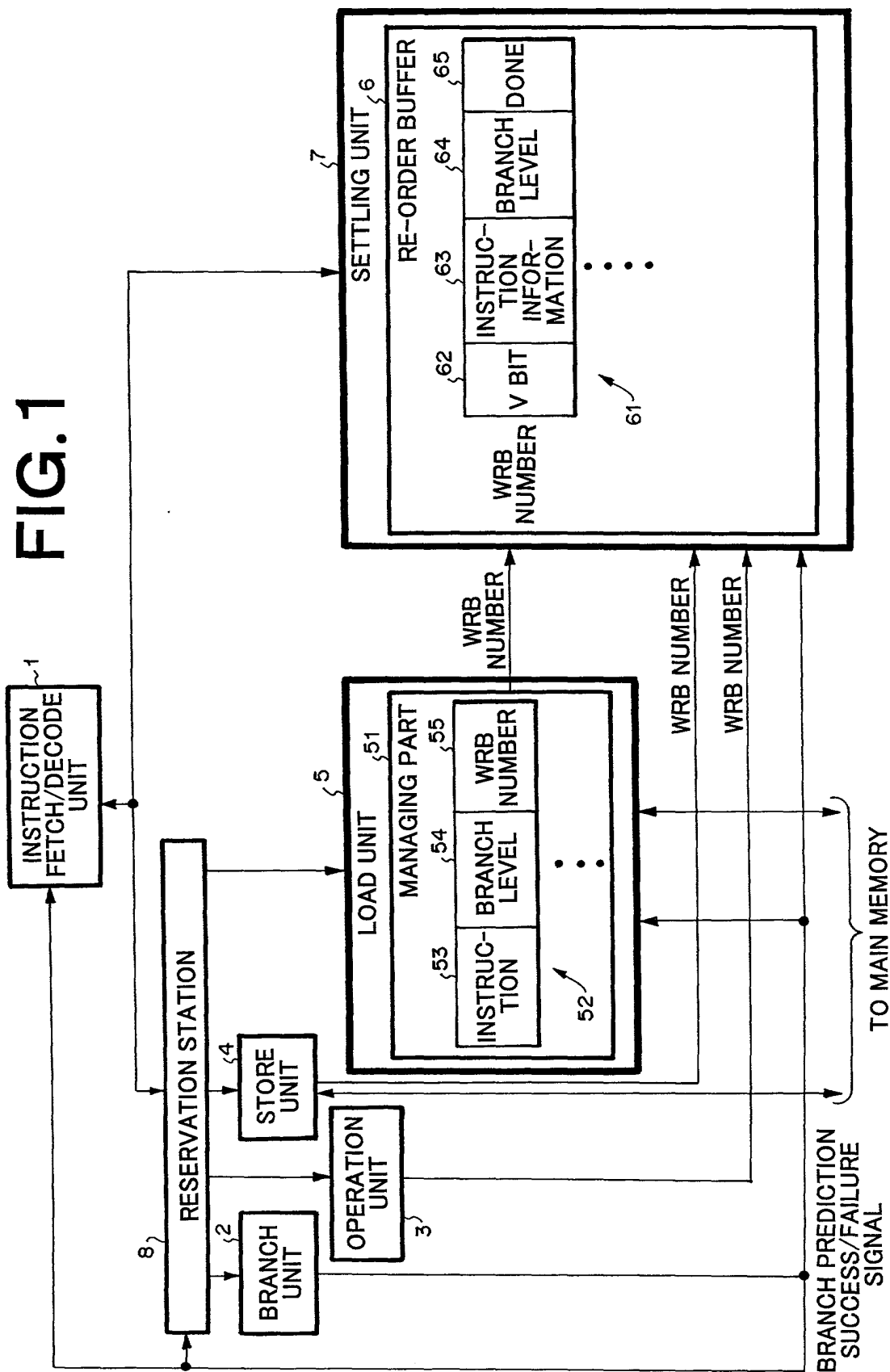


FIG.2

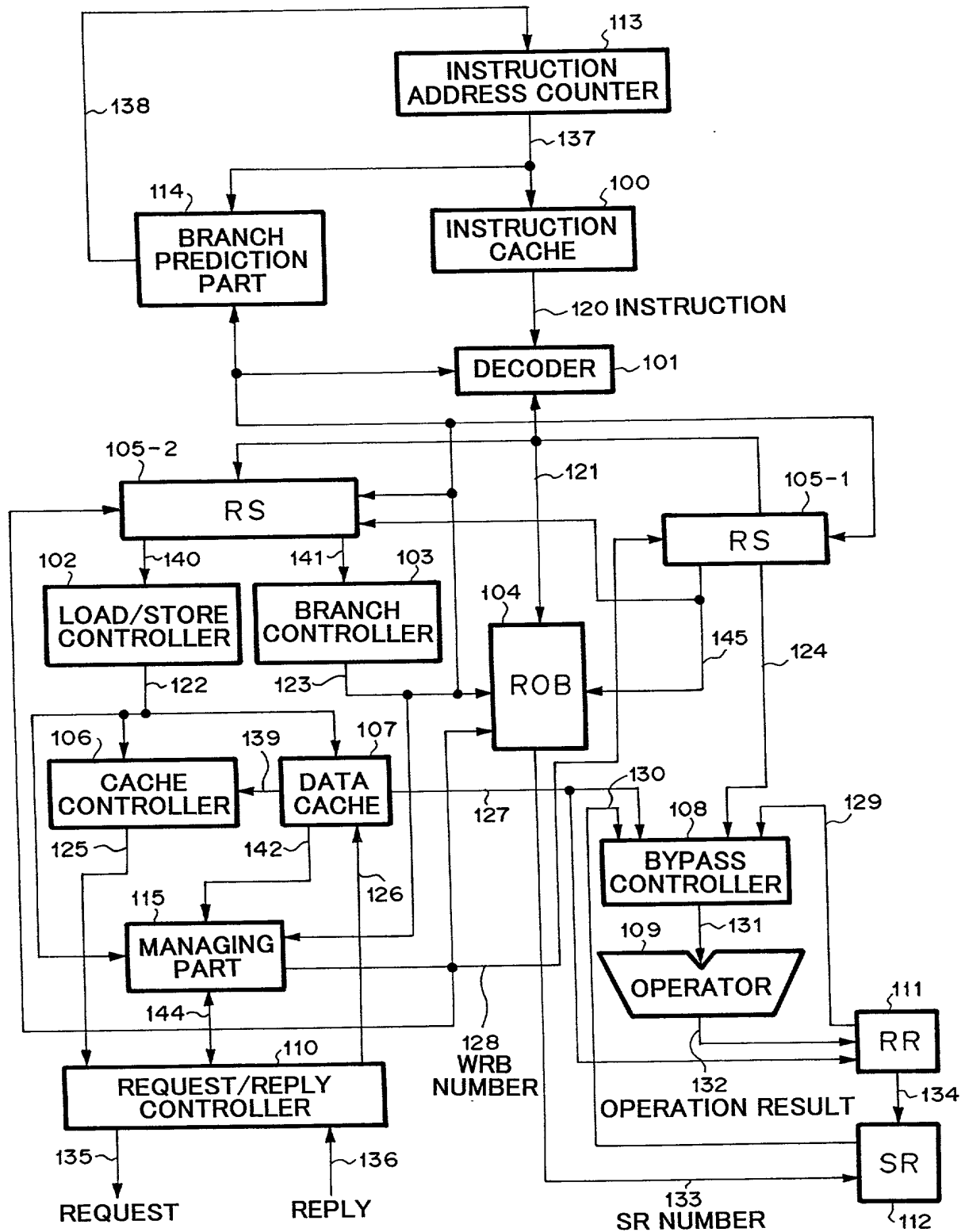


FIG. 3

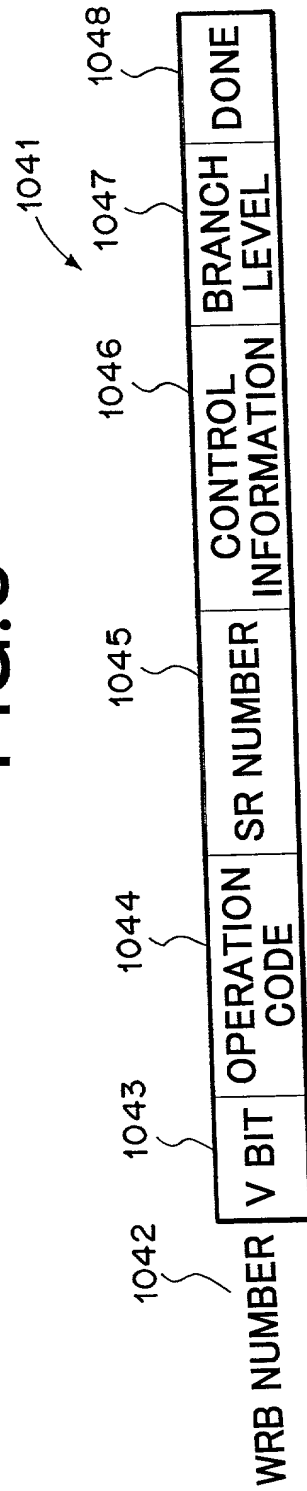


FIG.4A

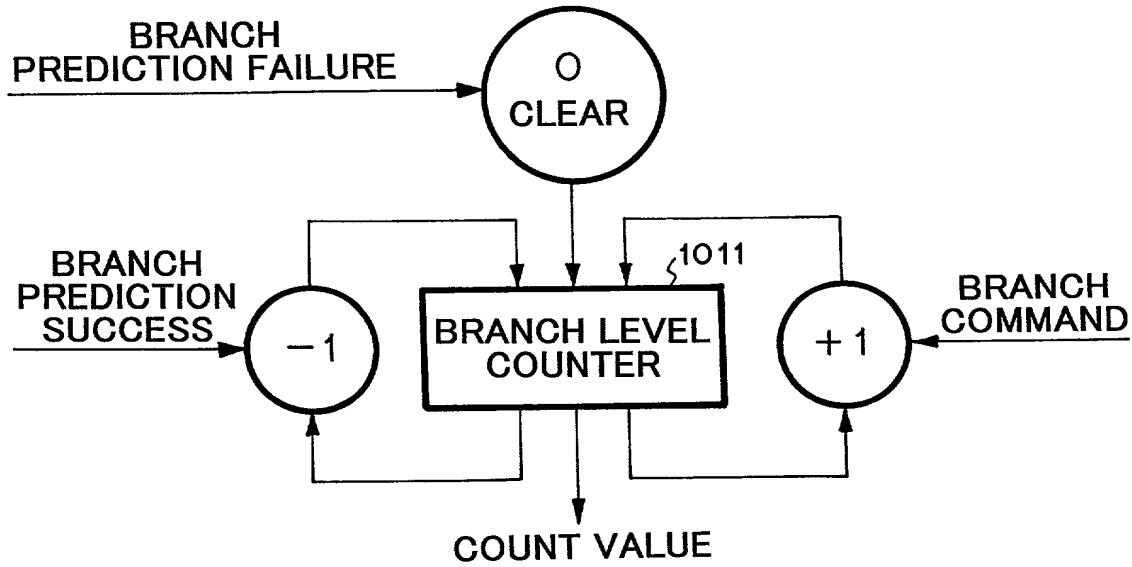


FIG.4B

INSTRUCTION	BRANCH LEVEL
LOAD INSTRUCTION 1	0
LOAD INSTRUCTION 2	0
OPERATION INSTRUCTION 1	0
STORE INSTRUCTION 1	0
BRANCH INSTRUCTION 1	0
OPERATION INSTRUCTION 2	1
OPERATION INSTRUCTION 3	1
LOAD INSTRUCTION 3	1
LOAD INSTRUCTION 4	1
OPERATION INSTRUCTION 4	1
BRANCH INSTRUCTION 2	1
LOAD INSTRUCTION 5	2
LOAD INSTRUCTION 6	2
OPERATION INSTRUCTION 5	2
BRANCH INSTRUCTION 3	2

FIG. 5

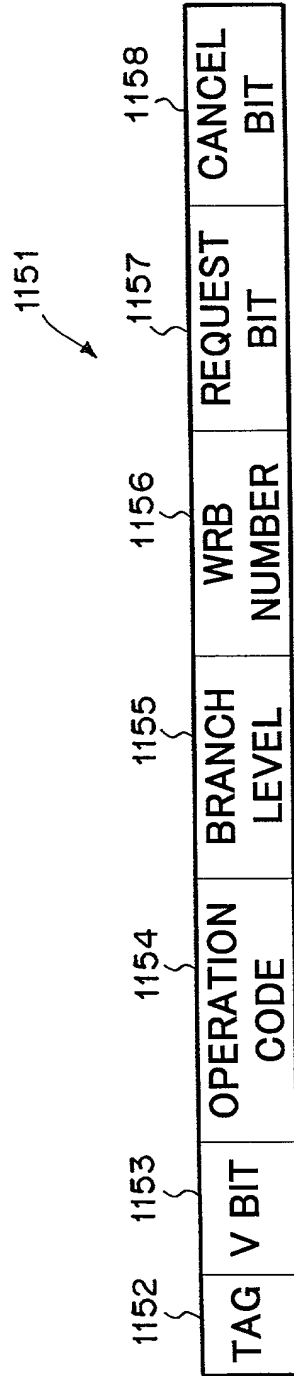


FIG. 6

WRB NUMBER	V BIT	OPE CODE	BRANCH LEVEL	DONE
00	0	LOAD INSTRUCTION 1	0	1
01	0	LOAD INSTRUCTION 2	0	1
02	0	OPERATION INSTRUCTION 1	0	1
03	0	STORE INSTRUCTION 1	0	1
04	1	BRANCH INSTRUCTION 1	0	0
05	1	OPERATION INSTRUCTION 2	1	0
06	1	OPERATION INSTRUCTION 3	1	0
07	1	LOAD INSTRUCTION 3	1	0
08	1	LOAD INSTRUCTION 4	1	0
09	1	OPERATION INSTRUCTION 4	1	0
0B	1	BRANCH INSTRUCTION 2	1	0
0C	1	LOAD INSTRUCTION 5	2	0
0D	1	LOAD INSTRUCTION 6	2	0
0E	1	OPERATION INSTRUCTION 5	2	0
10	1	BRANCH INSTRUCTION 3	2	0
11	0	-----	0	1
12	0	-----	0	1
13	0	-----	0	1
14	0	-----	0	1
15	0	-----	0	1
16	0	-----	0	1